



March 9, 2004

Mail Stop Issue Fee
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicants: Jack H. Yuan, Eliyahou Harari, Yupin K. Fong and George Samachisa
Assignee: SanDisk Corporation
Title: Scalable Self-Aligned Dual Floating Gate Memory Cell Array and
Methods of Forming the Array
Application No.: 09/925,102 Filing Date: August 8, 2001
Examiner: Dang, Phuc T. Group Art Unit: 2818
Docket No.: SNDK.257US0 Conf. No.: 3186

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (in duplicate);
- (3) Issue Fee Transmittal (1 page-in duplicate);
- (4) ReSubmission of Formal Drawings (1 page);
- (5) 12 sheets of formal drawings;
- (6) Copies of previously filed documents: Return Receipt Postcard and Express Mail Ticket
(1 page), Response to Notice of Incomplete Reply (2 pages), Submission of Formal Drawings
(1 page); and
- (7) Check for \$1,339.00.

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<input type="checkbox"/> Publication Fee	\$	--
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P.O. Box 1450, Alexandria, VA 22313-1450,
on March 9, 2004

Mary S. Buggi
Signature

Respectfully submitted,

Gerald P. Parsons
Reg. No.: 24,486